

CLAIM LISTING

1. (Currently Amended) A system for displaying images on a display, said system comprising:

a decoder for decoding encoded images and parameters associated with the images, thereby resulting in decoded images and decoded parameters associated with the decoded images;

image buffers for storing the decoded images;

parameter buffers for storing the decoded parameters associated with the images, ~~wherein the decoded parameters include at least one parameters selected from a group consisting of presentation time stamp, top field first, and repeat first field;~~ and

a display engine for receiving the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers.

2. (Previously Presented) The system of claim 1, wherein the encoded images and the parameters associated with the images form portions of data packets.

3. (Original) The system of claim 2, wherein the data packets comprise headers, wherein the headers comprise the parameters.

4. (Original) The system of claim 3, wherein the headers comprise picture layer headers.

5. (Original) The system of claim 3, wherein the headers comprise sequence layer headers.

6. (Original) The system of claim 2, wherein the data packets are associated with first headers and second headers, wherein the first headers comprise a portion of the parameters, and wherein the second headers comprise another portion of the parameters.

7. (Original) The system of claim 6, wherein the first headers comprise picture layer parameters and wherein the second headers comprise sequence layer parameters.

8. (Original) The system of claim 1, wherein the encoded images comprise compressed images.

9. (Original) The system of claim 1, wherein the parameters are encoded with a variable length code, and wherein the decoder decodes the variable length code.

10. (Previously Presented) A circuit for displaying images on a display, said circuit comprising:

a decoder;

image buffers connected to the decoder and configured to store images decoded by the decoder;

parameter buffers connected to the decoder and configured to store parameters associated with the images and decoded by the decoder; and

a display engine connected to the image buffers and the parameter buffers and configured to receive the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers.

11. (Previously Presented) The circuit of claim 10, wherein the encoded images and the parameters associated with the images form portions of data packets.

12. (Original) The circuit of claim 11, wherein the data packets comprise headers, wherein the headers comprise the parameters.

13. (Original) The circuit of claim 12, wherein the headers comprise picture layer headers.

14. (Original) The circuit of claim 12, wherein the headers comprise sequence layer headers.

15. (Original) The circuit of claim 11, wherein the data packets are associated with first headers and second headers, wherein the first headers comprise a portion of the parameters, and wherein the second headers comprise another portion of the parameters.

16. (Original) The circuit of claim 15, wherein the first headers comprise picture layer parameters and wherein the second headers comprise sequence layer parameters.

17. (Cancelled)

18. (Original) The circuit of claim 10, wherein the parameters are encoded with a variable length code, and wherein the decoder decodes the variable length code.

19-26. (Cancelled).

Please add the following claims:

--27. (New) The system of claim 1, wherein the decoded parameters include at least one parameters selected from a group consisting of presentation time stamp, top field first, and repeat first field.--